



TRANSMITTAL LETTER  
(General - Patent Pending)

Docket No.  
BUR920000228US1

In Re Application No. Malinowski et al.

Serial No.  
09/683,027

Filing Date  
11/9/01

Examiner  
Thai, Luan C.

Group Art Unit  
2827

Title: DUAL CHIP STACK METHOD FOR ELECTRO-STATIC DISCHARGE PROTECTION OF  
INTEGRATED CIRCUITS

TO THE COMMISSIONER FOR PATENTS:

Transmitted herewith is:

Petition Under 37 CFR 1.181(a)- 1 page

RCE- 1 page

Copy of Response filed on December 9, 2002- 12 pages (including confirmation)

in the above identified application.

- No additional fee is required.
- A check in the amount of \_\_\_\_\_ is attached.
- The Director is hereby authorized to charge and credit Deposit Account No. 09-0456(IBM) as described below.
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Signature

Dated: 11/10/2003

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I certify that this document and fee is being deposited on 11/10/03 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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